## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant: Art Unit: 2195

Hong Jiang et al. Examiner: Eric Charles Wai

Serial No.: 10/750,583 Conf. No.: 8582

Filed: December 31, 2003

Docket: ITL.1704US For: Processing Architecture Having P17510

Passive Threads and Active

Semaphores **Intel Corporation** Assignee:

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed March 13, 2009, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made to the claims.

Date of Deposit: May 12, 2009

I hereby certify that this correspondence is being electronically transmitted on the date indicated above. Cynthia L. Hayden